

Fig. 1

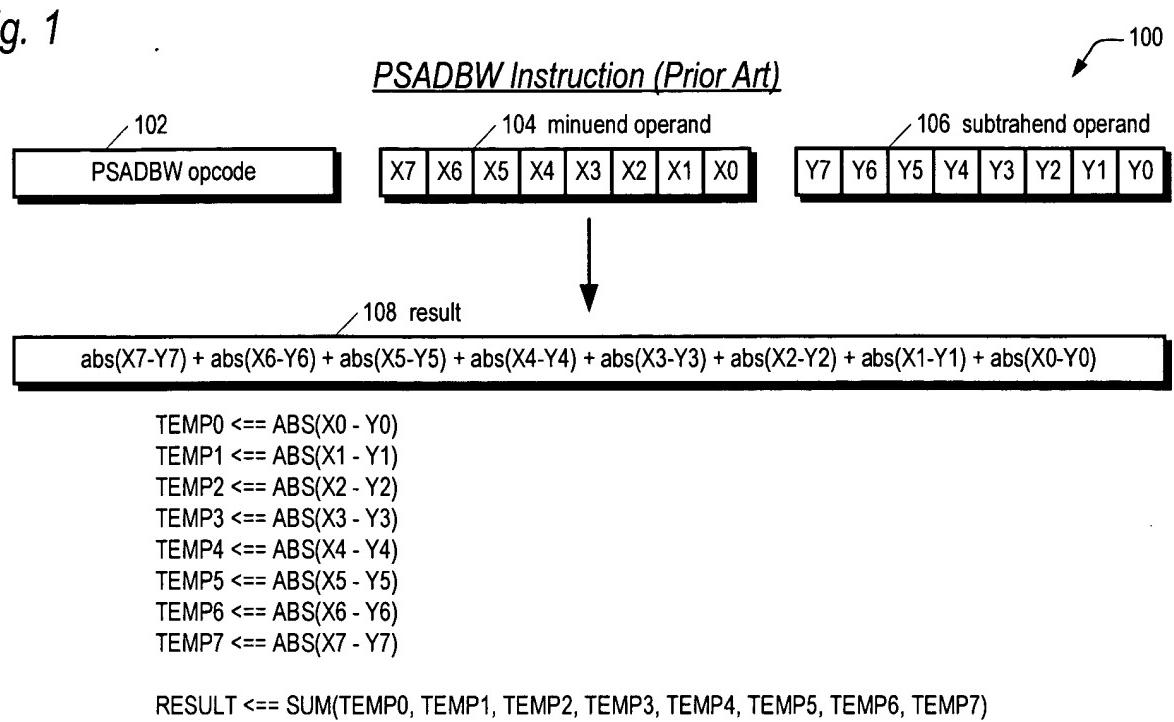


Fig. 2

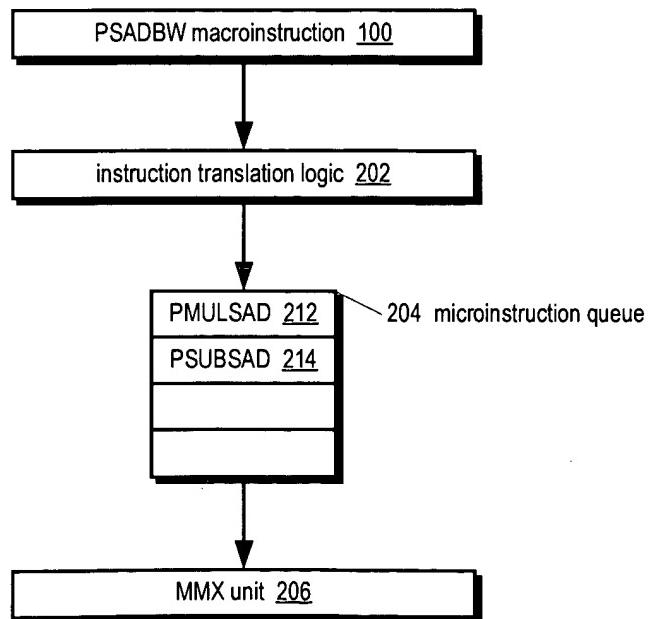
Microprocessor

Fig. 3

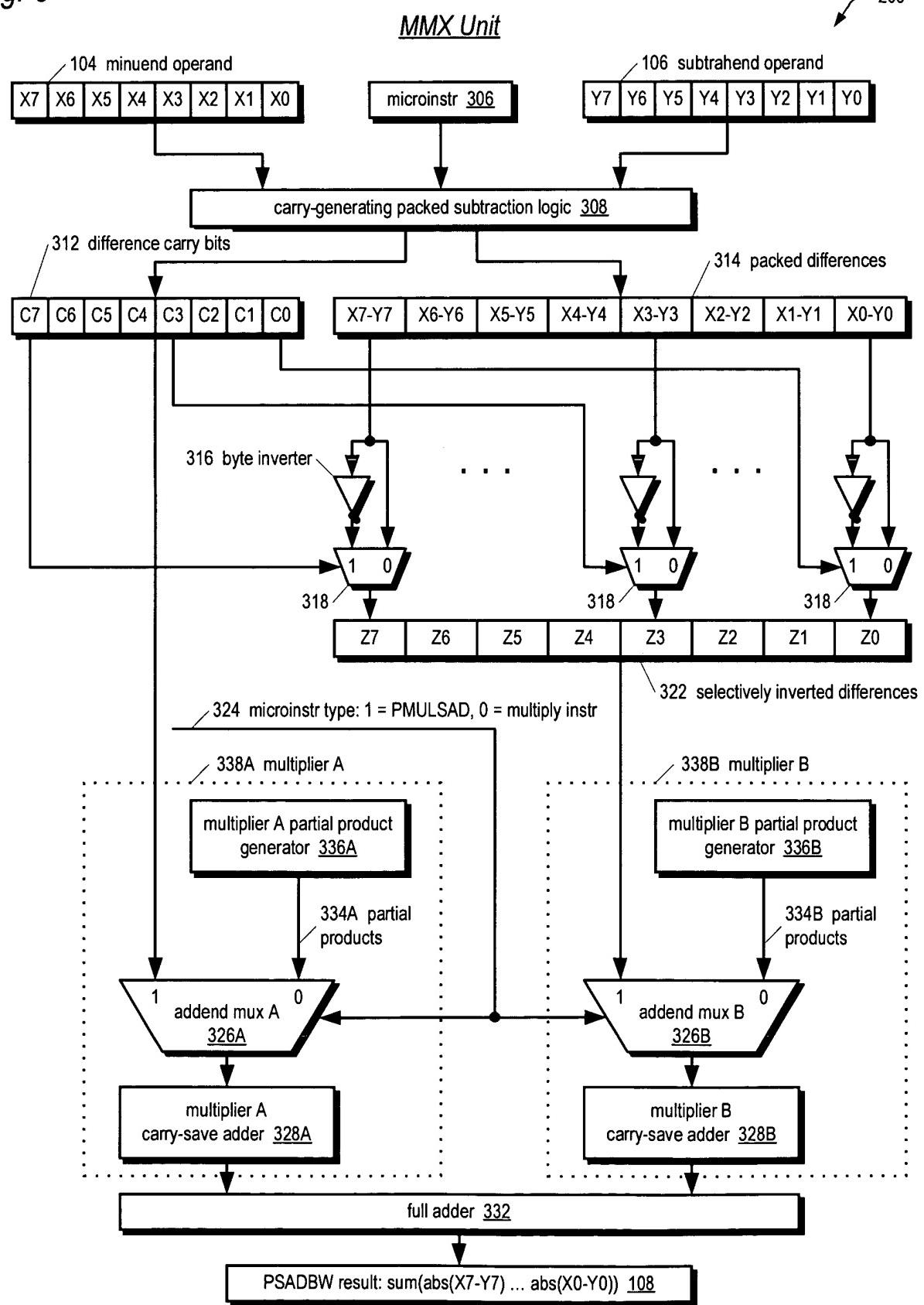


Fig. 4

Generation of Packed Sum of Absolute Differences